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CSS 2016-07 this thesis evaluates the viability of ferroelectric si hfo2 and its derived fet application for non volatile data storage at the beginning the ferroelectric effect is explained briefly such that the applications that make use of it can be understood afterwards the latest findings on ferroelectric hfo2 are reviewed and their potential impact on future applications is discussed experimental data is presented afterwards focusing on the ferroelectric material characteristics of si hfo2 that are most relevant for memory applications besides others the stability of the ferroelectric switching effect could be demonstrated in a temperature range of almost 400 k moreover nanosecond switching speed and endurance in the range of 1 million to 10 billion cycles could be proven retention and imprint characteristics have furthermore been analyzed and are shown to be stable for 1000 hours bake time at 125 oc derived from the ferroelectric effect in hfo2 a 28 nm fet memory cell is introduced as the central application of this thesis based on numerical simulations the memory concept is explained and possible routes towards an optimized fet cell are discussed subsequently the results from electrical characterization of fet multi structures are presented and discussed by using si hfo2 it was possible to realize the world s first 28 nm fet devices possessing i a 10k cycling endurance and an extrapolated 10 year data retention at room temperature the next step towards a fet memory is represented by connecting several memory cells into matrix type configurations a cell concept study illustrates the different ways in which fet cells can be combined together to give high density memory arrays for the proposed architectures operational schemes are theoretically discussed and analyzed by both electrical characterization of fet multi structures and numerical simulations the thesis concludes with the electrical characterization of small fet memory arrays first results show that a separation between memory states can be achieved by applying poling and incremental step pulse programming ispp sequences these results represent an important cornerstone for future studies on si hfo2 and its related applications

Development of HfO2-Based Ferroelectric Memories for Future CMOS Technology

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