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exercise 4 4 e f sedra smith diode logic gates in this video i have tried to explain problem solving techniques for diode and gate and or gate diode and gate or gate

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figure e5 10 shows a circuit obtained by augmenting the circuit of fig e5 9 considered in exercise 5 9 with a transistor  $Q_2$  identical to  $Q_1$  and a resistance  $r_2$

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note that the equations in sedra smith for the ce amplifier are in terms of  $r_\pi$  and not  $r_e$  input resistance  $r_{iB}$   $r_{iE}$   $r_{iC}$   $r_{oC}$   $r_{oE}$   $r_{oB}$  where  $r_{oC} = r_{oE} = r_{oB} = r_o$   $r_{iB} = r_\pi + (\beta + 1)r_e$   $r_{iE} = r_e$   $r_{iC} = r_\pi$   $r_{oC} = r_o + r_e$   $r_{oE} = r_o$   $r_{oB} = r_o$

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you can use the same slow motion technique on problem e4 4 f in your question in that case assume the voltage at the top of the resistor was  $0V$  and then suddenly switched on to  $5V$  initially the anodes of all 3 diodes will be  $0V$  and then begin to rise which diode will start to conduct first

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